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1. Field of the Invention The present invention relates to a method for manufacturing a semiconductor light emitting device which includes an anti-reflection layer that is formed on a light emitting portion having a cavity structure formed by etching a semiconductor crystal to improve the light extraction efficiency of the light emitting portion and to a semiconductor light emitting device

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outgrowth was observed, which was markedly influenced by growth treatments. A clear difference of dendritic tree size distribution between neurons was observed, with polarized neurons showing longer and broader dendritic branches, especially at the distal pole. In addition, polarized neurons were always faster than unpolarized neurons. Given the recognized importance of these parameters for neuronal connectivity, this work lays the basis for a multiscale model that takes into account the morphometric aspects of neuronal polarization. This new model of neurite outgrowth considers the complexity of the process for the first time, in terms of cell size and morphology, by introducing explicit kinetic equations for the different components involved in the process, and by integrating the complexity of cellular organization at all scales, from the individual cell to the entire population.

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